	Application No.	Applicant(s)
Notice of Allowability	09/933,801	INOUE ET AL.
	Examiner	**Art Unit**	
	Paula W Klimach	2135	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to *02/22/05*.

2. ☒ The allowed claim(s) is/are *2-15*.

3. ☒ The drawings filed on *22 August 2001* are accepted by the Examiner.

4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a)☐ All   b)☐ Some*   c)☐ None   of the:

        1. ☐ Certified copies of the priority documents have been received.

        2. ☐ Certified copies of the priority documents have been received in Application No. _____ .

        3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the

            International Bureau (PCT Rule 17.2(a)).

    * Certified copies not received: _____ .

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. **THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.

    (a)☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached

        1)☐ hereto or 2)☐ to Paper No./Mail Date _____ .

    (b)☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of

        Paper No./Mail Date _____ .

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)

2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)

3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date *05/17/04*

4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material

5. ☐ Notice of Informal Patent Application (PTO-152)

6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____ .

7. ☒ Examiner's Amendment/Comment

8. ☐ Examiner's Statement of Reasons for Allowance

9. ☐ Other _____ .

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Carl Brundidge on 03/31/05.

Claims should read

2. A program writable IC card comprising:

a microprocessor;

a first memory which stores both a first program to be executed by said microprocessor and a decryption program, executable by said microprocessor, having a decryption function,

wherein said first memory is a read only memory (ROM) which is not re-writable, and

a second memory capable of storing a second program,

wherein said microprocessor applies said decryption function to an encrypted second program, which has been encrypted based on an encryption key, according to said decryption program and then stores a decrypted second program, which is executable by said microprocessor, in said second memory, when said encrypted second program is provided from outside the IC card,

wherein when said encrypted second program is not encrypted correctly, writing

[said] of said encrypted second program is rejected.

3. A program writable IC card, comprising:

a microprocessor;

a first memory which stores both a first program which is executed by said

microprocessor and a write control program, executable by said microprocessor,

having a decryption function,

wherein said first memory is a read only memory (ROM) which is not re-

writable, and

a second memory in which may be written a second program,

wherein said microprocessor applies said decryption function to an encrypted

second program, which has been encrypted based on an encryption key, and then

writes a decrypted second program, which is executable by said microprocessor, in

said second memory according to said write control program, when said encrypted

second program is inputted from outside the IC card,

wherein when said encrypted second program is not encrypted correctly, [said]

writing of said encrypted second program is rejected.

4. A program writable IC card comprising:

a microprocessor;

a first memory which stores a first program which is executed by said

microprocessor and a second program, executable by said microprocessor, for

decrypting an encrypted program,

wherein said first memory is a read only memory (ROM) which is not re-writable;

a second memory which is able to store a third program; and

an input unit which inputs an encrypted third program, which has been encrypted based on an encryption key, from outside the IC card,

wherein said microprocessor applies said second program to said encrypted third program inputted by said input unit, stores a decrypted third program which is executable by said microprocessor in said second memory and then executes said decrypted third program stored in said second memory, and

wherein when said encrypted third program is not encrypted correctly, [said] writing of said encrypted third program is rejected.

5. A program writable IC card, comprising:

a microprocessor;

a first memory which stores a first program having a decryption function which is executed by said microprocessor,

wherein said first memory is a read only memory (ROM) which is not re-writable; and

a second memory which is able to store a second program,

wherein said microprocessor applies said decryption function to an encrypted second program, which has been encrypted based on an encryption key, according

to said first program and then stores a decrypted second program which is

executable by said microprocessor in said second memory, wherein said encrypted

second program is initially supplied from outside the IC card, and

wherein when said encrypted second program is not encrypted correctly, [said]

writing of said encrypted second program is rejected.

6. A program writable IC card according to claim 2, wherein said first program performs

an original function of the IC card. [, and wherein when said encrypted second program is not

encrypted correctly, said writing of said encrypted second program is rejected.]

7. A program writable IC card according to claim 3, wherein said first

program is an IC card function program for realizing an original function of the IC card. [, and

wherein when said encrypted second program is not encrypted correctly, said writing of said

encrypted second program is rejected.]

8. A program writable IC card according to claim 4, wherein said first

program performs an original function of the IC card. [and wherein when said encrypted third

program is not encrypted correctly, said writing of said encrypted third program is rejected.]

9. A program writable IC card according to claim 4, wherein said first

program performs an original function of the IC card. [, and wherein when said encrypted second

program is not encrypted correctly, said writing of said encrypted second program is rejected.]

10. A processing method for a program writable IC card having a
microprocessor, a first memory which stores both a first program which is executed
by said microprocessor and a decryption program having a decryption function,
wherein said first memory is a read only memory (ROM) which is not re-writable, and
a second memory, said processing method comprising:

inputting an encrypted second program, which has been encrypted based on
an encryption key, from outside the IC card;

decrypting said encrypted second program according to said decryption
program ;

storing a decrypted second program in said second memory, said decrypted
second program being executable by said microprocessor; and

rejecting [the] writing of an encrypted second program when said encrypted
second program is not encrypted correctly.

11. A processing method for a program writable IC card having a
microprocessor, a first memory which stores both a first program which is executed
by said microprocessor and a decryption program having a decryption function,
wherein said first memory is a read only memory (ROM) which is not re-writable, a
second memory, and an input unit, said processing method comprising:

inputting from an external device an encrypted second program, which has
been encrypted based on an encryption key, via said input unit;

decrypting said encrypted second program according to said decryption program;

storing a decrypted second program in said second memory, said decrypted second program being executable by said microprocessor;

executing said decrypted second program stored in said second memory; and

rejecting [the] writing of an encrypted second program when said encrypted second program is not encrypted correctly.

12. A processing method for a program writable IC card having a microprocessor, a first memory which stores a decryption program executed by said microprocessor, wherein said first memory is a read only memory (ROM) which is not re-writable, and a second memory, said processing method comprising:

inputting an encrypted second program, which has been encrypted based on an encryption key, from an external device;

decrypting said encrypted second program according to said decryption program; and

storing a decrypted second program in said second memory,

wherein said second memory can be written only once, and

wherein said decrypted second program is executable by said microprocessor; and

rejecting [the] writing of an encrypted second program when said encrypted second program is not encrypted correctly.

13. A writing method for an IC card having a microprocessor, a ROM device which stores a first program which is executed by said microprocessor, a write control program having a decryption function, and a writable memory, said writing method comprising:

inputting an encrypted second program, which has been encrypted based on an encryption key, from an external device;

decrypting said encrypted second program according to said write control program;

storing a decrypted second program in said writable memory according to said write control program, said decrypted second program being executed by said microprocessor;

rejecting [the] writing of an encrypted second program when said encrypted second program is not encrypted correctly.

14. A writing method for an IC card having a microprocessor, a ROM device which stores both a first program which is executed by said microprocessor, a decryption program having a decryption function, a writable memory, and an input unit, said writing method comprising:

inputting an encrypted second program, which has been encrypted based on an encryption key, via said input unit from an external device;

decrypting said encrypted second program according to said decryption

program;

storing a decrypted second program in said writable memory, said decrypted

second program being executable by said microprocessor;

executing said decrypted second program stored in said writable memory;

and

rejecting [the] writing of an encrypted second program when said encrypted

second program is not encrypted correctly.

Conclusion

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Paula W Klimach whose telephone number is (571) 272-3854.

The examiner can normally be reached on Mon to Thr 9:30 a.m to 5:30 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Kim Vu can be reached on (571) 272-3859. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PWK
Monday, April 04, 2005

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